

1/7

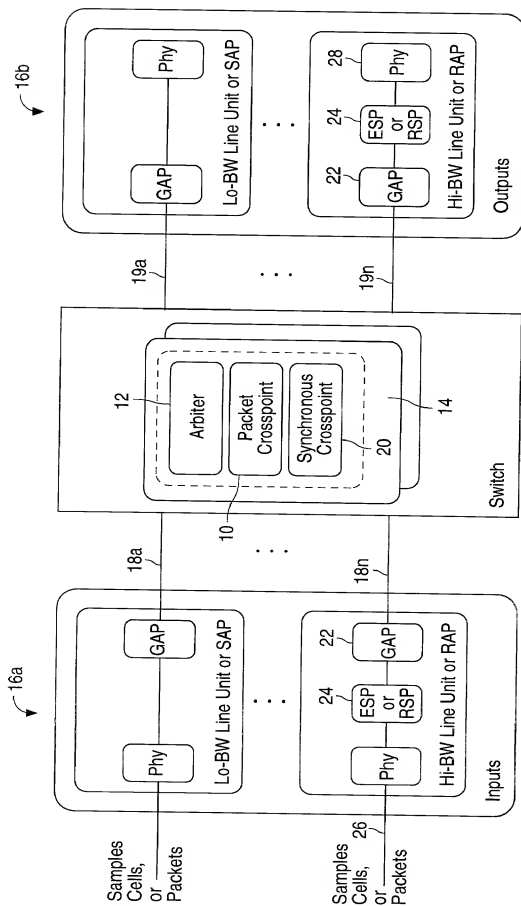


FIG. 1

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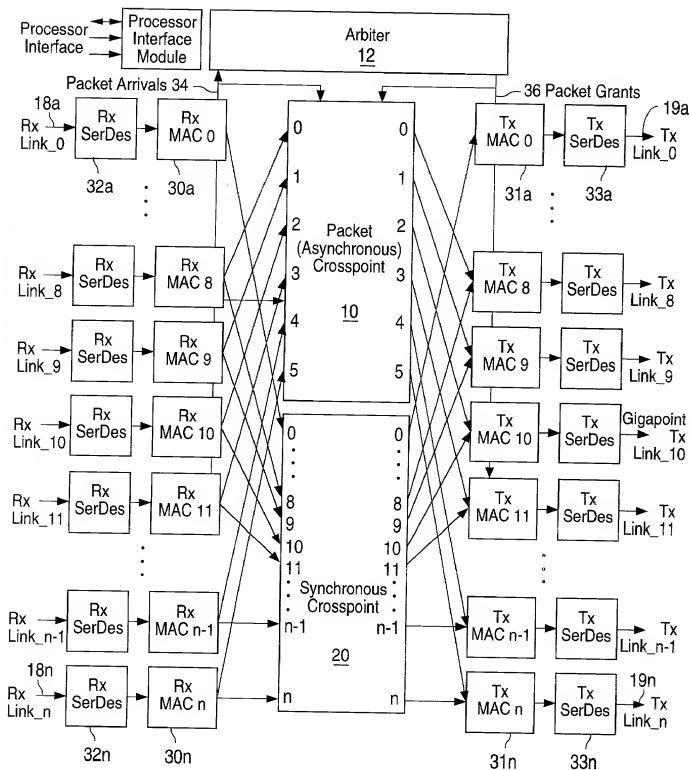


FIG. 2

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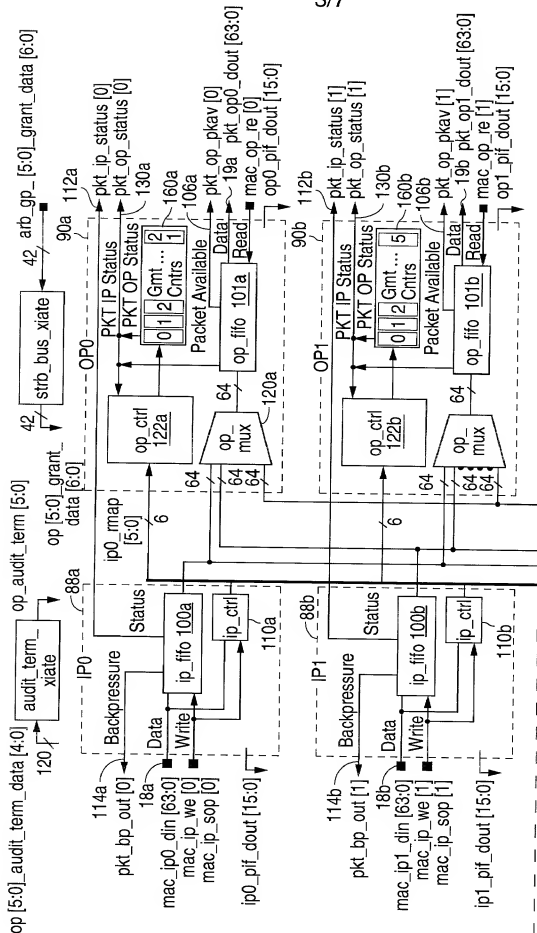


FIG. 3A

FIG. 3
 FIG. 3A
 FIG. 3B



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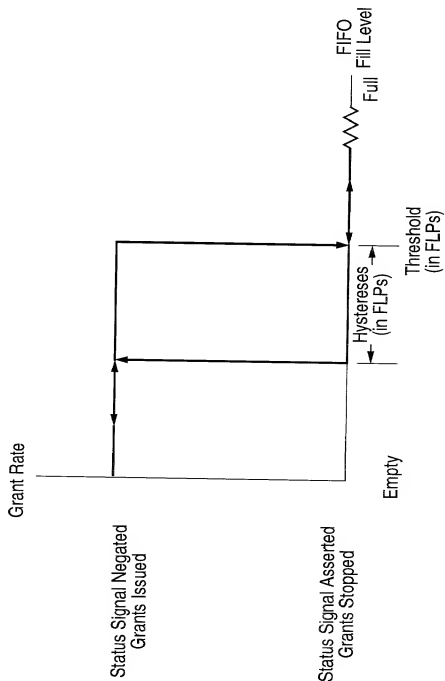


FIG. 4

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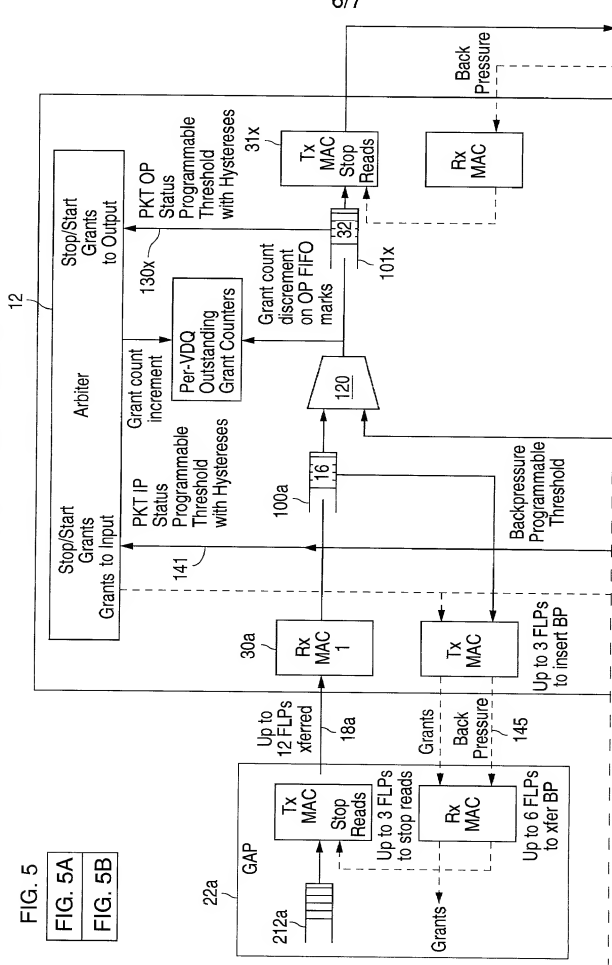


FIG. 5A

FIG. 5

FIG. 5A

FIG. 5B

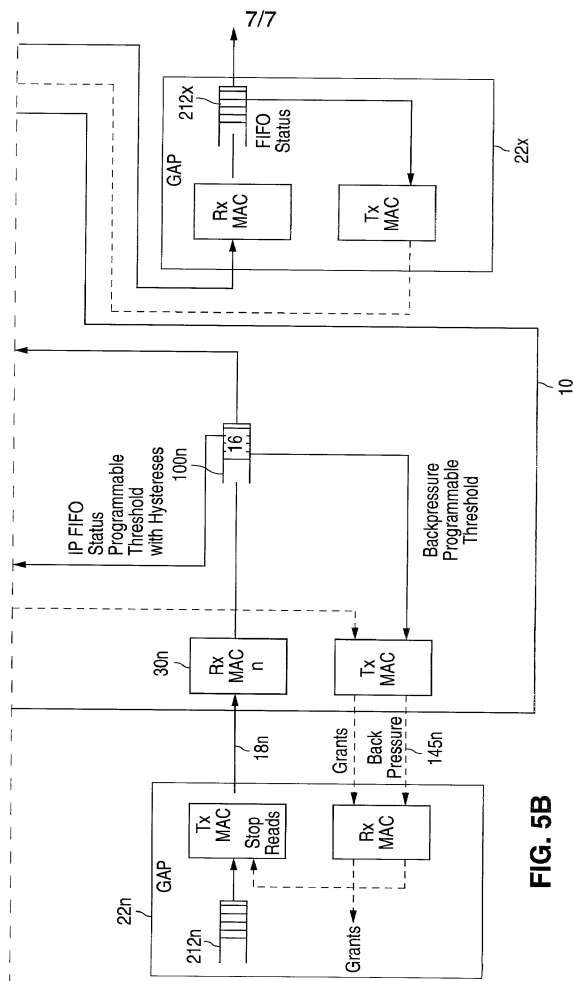


FIG. 5B

FIG. 5B is a block diagram of a multi-ported network architecture.